AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) An active matrix LCD having

column electrodes for display signals;

row electrodes for scanning, the row electrodes being orthogonal to the column electrodes;

pixels arranged in a matrix at intersections of the column and row electrodes;

a column driver to sequentially supply~~, in each horizontal scan period, display signals~~ to the column electrodes display signals in a display signal period of each horizontal scan period and a reset voltage in part or in whole, of a horizontal blanking period of each horizontal scan period; and

a row driver to sequentially supply row select pulses to the row electrodes,

wherein the row driver ~~comprising~~ comprises:

a first shift register to sequentially generate first row select pulses ~~in respective display signal periods of a vertical scan period~~ in response to a first scan start signal;

a first gate circuit to supply the first row select pulses to the row electrodes so that the display signals in respective display signal periods are written in ~~a row~~ respective rows of the pixels;

a second shift register to sequentially generate second row select pulses ~~that reset pixels to a reset voltage~~ in response to a second scan start signal; and

a second gate circuit to supply the second row select pulses to the row electrodes~~, in part or in whole, of respective horizontal blanking periods of the vertical scan period~~ so that the reset voltage in respective horizontal blanking periods are written in the respective rows of the pixels.

2. (Currently amended) The active matrix LCD of claim 1, wherein the column driver comprises:

a level setter configured to partly or wholly set [[a]] the horizontal blanking period of the horizontal scan period as a period to provide the reset voltage; and

an output unit configured to turn on all switches of the column driver in a reset period during which display signals have no image information, and in cooperation with the level setter, supply the reset voltage to all of the column electrodes; and

the row driver is:

configured to sequentially provide the first and second row select pulses to select the row electrodes one after another for each horizontal scan period including ~~a first~~ the display signal period during which the column driver provides the column electrodes with the display signals having image information and ~~a second~~ part or whole of the horizontal blanking period during which the output unit provides the column electrodes with the reset voltage such that an absolute value of voltage accumulated in each pixel due to the display signal is below a predetermined value in each vertical scan period.

3. (new) The active matrix LCD of claim 1, wherein each of the second row select pulses is supplied to each of the row electrodes after a predetermined plurality of horizontal scan periods after each of the first row select pulses is supplied to each of the row electrodes so that the pixels in each row are reset after the predetermined plurality of horizontal scan periods after the display signals are written in the pixels in each row.